

IN THE SPECIFICATION:

Please amend the paragraph beginning on page 4, line 21 to page 5, line 19.

According to claim 2 of the present invention, a signal processing apparatus comprises a variable gain amplifier for automatically adjusting a signal read from a recording medium so that the signal has a desired amplitude; a filter circuit for --removing a signal in a specific band, the filter circuit being connected to the variable gain amplifier; an A/D converter for converting an analog signal into a digital signal, the converter being connected to the filter circuit; an automatic gain controller being connected to the A/D converter; a waveform equalizer for performing waveform equalization, the equalizer being connected to the A/D converter; a control circuit for performing baseline control for the output of the waveform equalizer and the output of the A/D converter, on the basis of the output of the waveform equalizer; an adaptive transversal filter for amplifying a signal in a specific band as well as performing waveform equalization for a reproduction signal, the filter being connected to the A/D[[/]] converter; a detection circuit for performing error detection and correction using a LMS (Least Mean Square) algorithm, the detection circuit being connected to the adaptive transversal filter; a decoder for performing maximum likelihood decoding, the decoder being connected to the adaptive transversal filter; and a timing recovery logic circuit for extracting a reproduction clock, the logic circuit being connected to the control circuit.